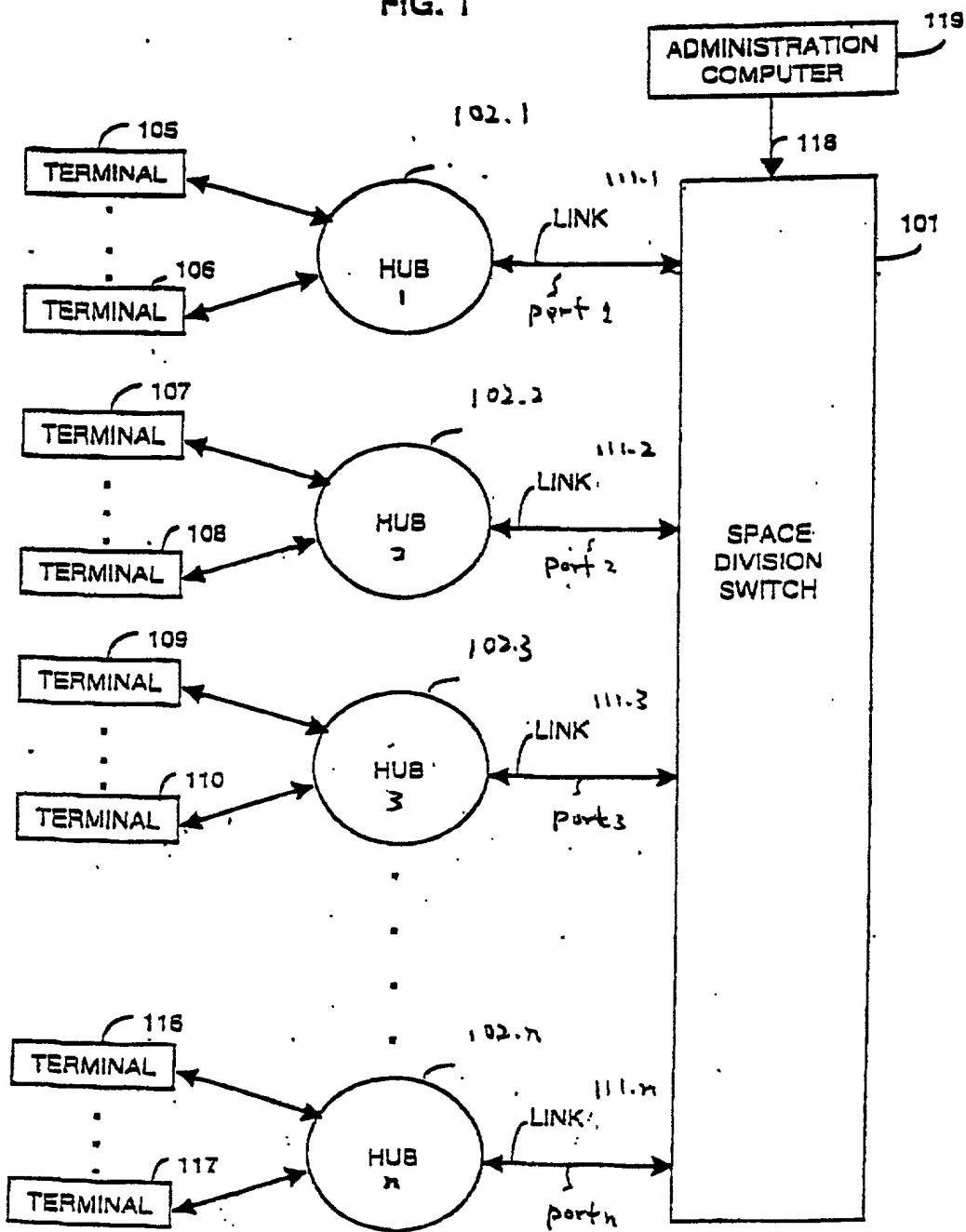
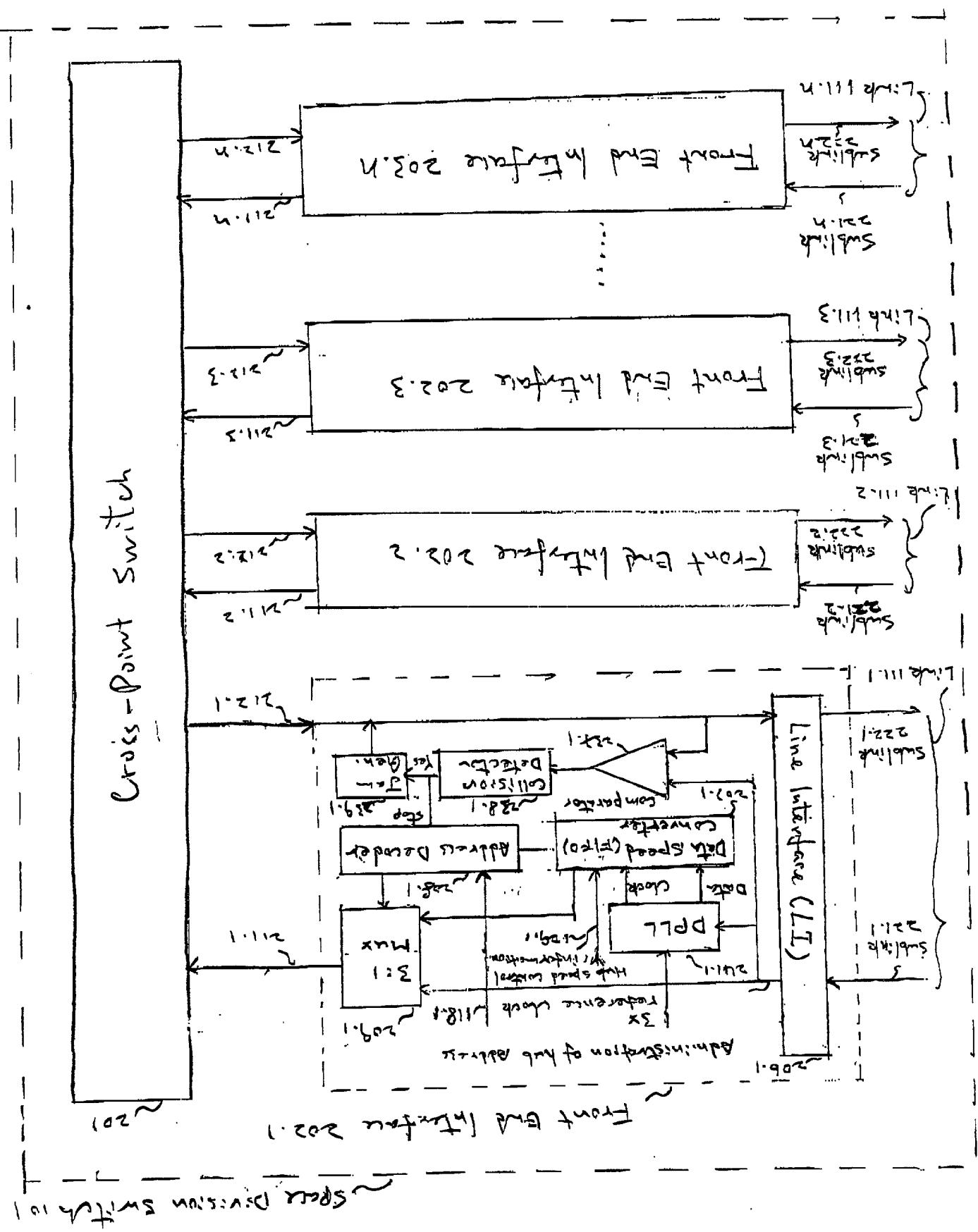


FIG. 1



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FIG. 3

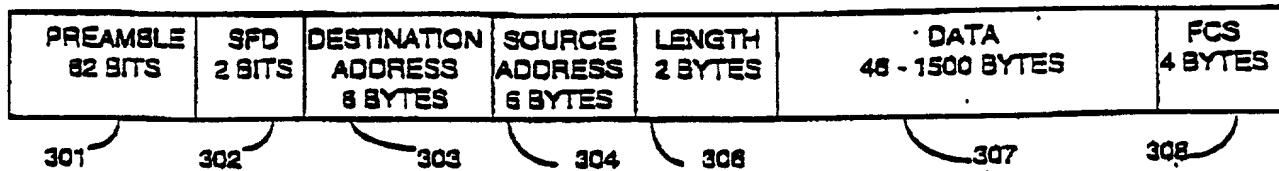


FIG. 4



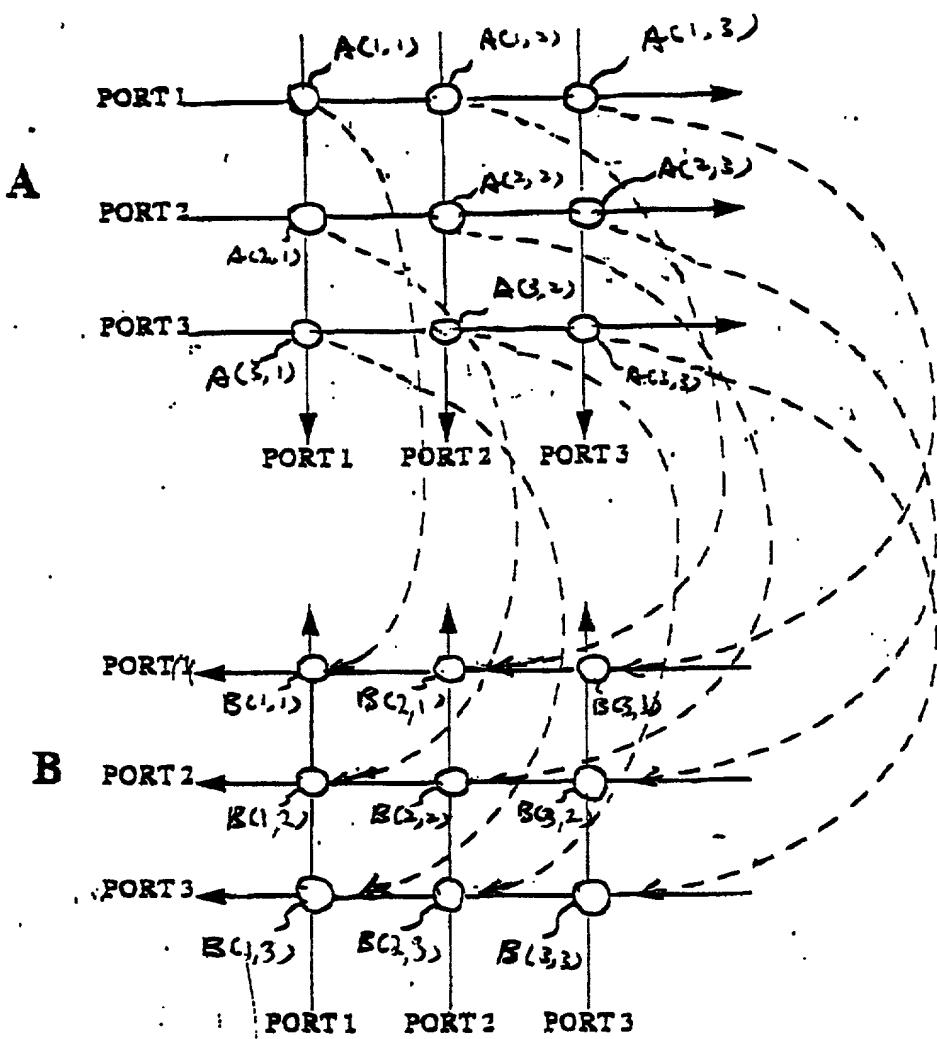
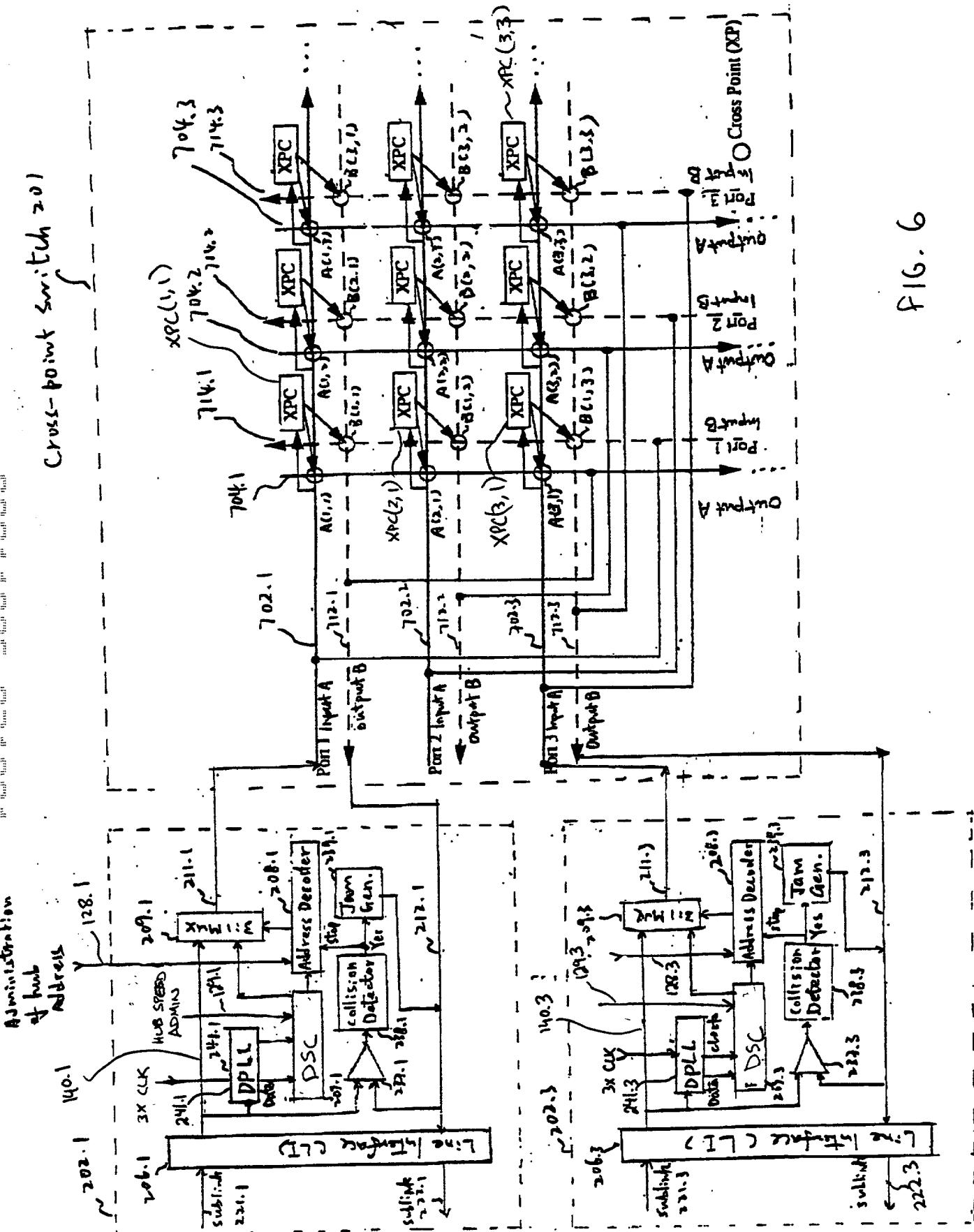


FIG. 5



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Matrix A

XPC(1,1)

XPC(1,3)

~704.3

OP

OP

DA

DA

COL

CH  
(B)CH  
(A)

COL

CH  
(B)CH  
(A)CH  
(B)

TO

BC(1,1)

BIA

TO

BC(3,1)

Matrix  
A

Port(s) →

211-1

XPC(3,1)

AC(1,1)

702.1

AC(3,3)

XPC(3,3)

Matrix  
A

Port(3) →

211-3

XPC(3,2)

212-1

212-3

FIG. 7

## Hub speed control information

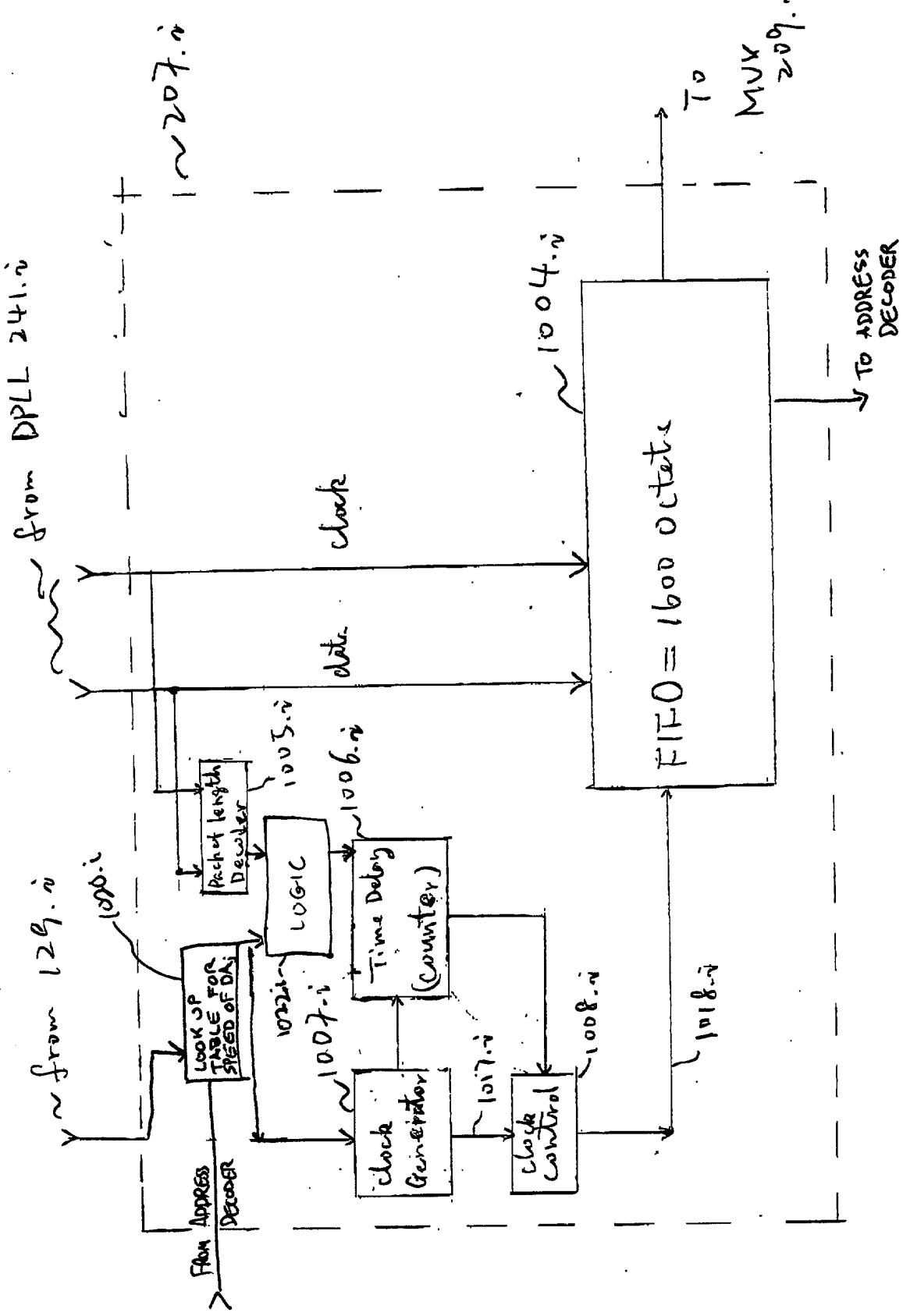
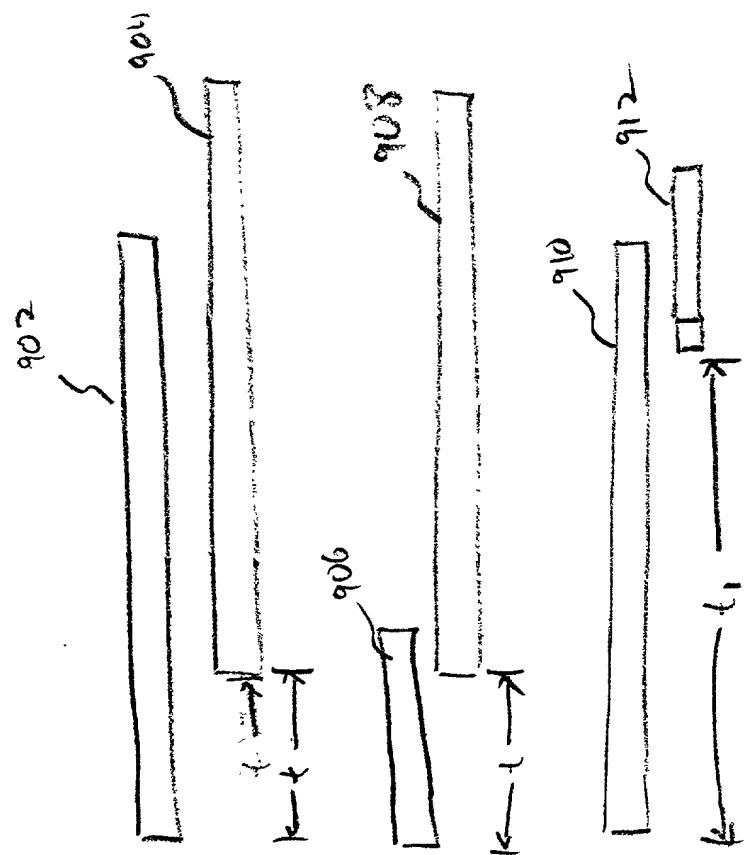


FIG. 8

Fig. 9



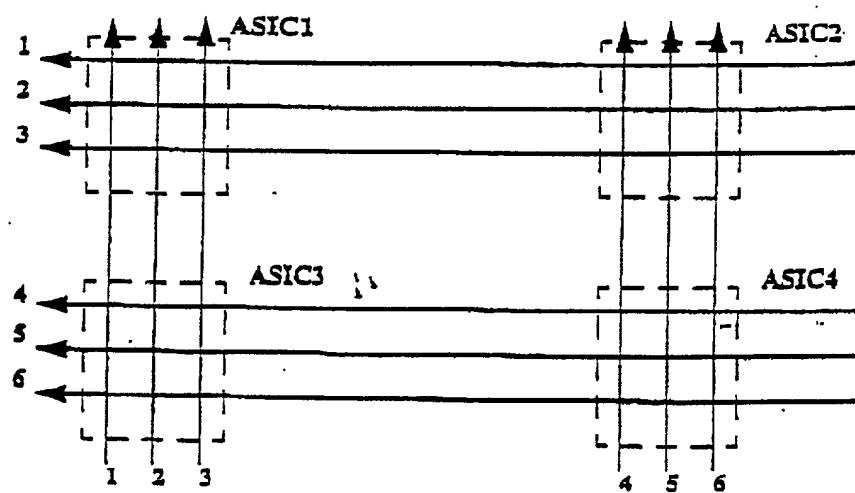
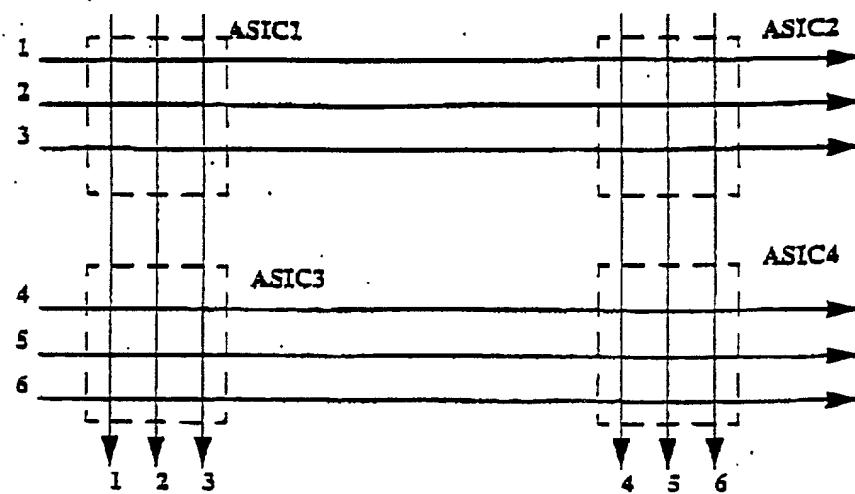


FIG. 10